



FIG. 19

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RAM4	RAM3		RAM2		RAM1		RAM0		HWY0
Spare RAM Bank	F r a m e	F r a m e	F r a m e	F r a m e	F r a m e	F r a m e	F r a m e	F r a m e	
	7	6	5	4	3	2	1	0	

FIG. 20

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